



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Chiharu IRIGUCHI

Application No.: 10/790,221

Docket No.: 118580

Filed: March 2, 2004

For: ALIGNER, EXPOSING METHOD, METHOD FOR MANUFACTURING THIN-FILM TRANSISTOR, DISPLAY DEVICE, AND ELECTRONIC DEVICE USING SHADING MEANS

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff  
Registration No. 27,075

Michael Britton  
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JAO:MB/al

Date: June 2, 2004

**OLIFF & BERRIDGE, PLC**  
**P.O. Box 19928**  
**Alexandria, Virginia 22320**  
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<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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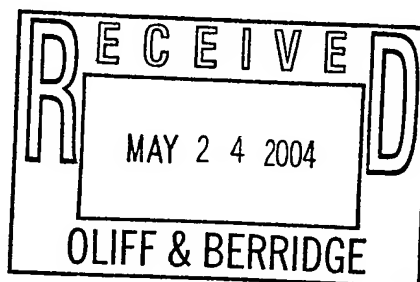


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/790,221	03/02/2004	1756	770	118580	9	13	3

25944  
OLIFF & BERRIDGE, PLC  
P.O. BOX 19928  
ALEXANDRIA, VA 22320



CONFIRMATION NO. 9847

FILING RECEIPT



\*OC000000012718164\*

Date Mailed: 05/20/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

Chiharu Iriguchi, Suwa-shi, JAPAN;

**Assignment For Published Patent Application**

SEIKO EPSON CORPORATION, TOKYO, JAPAN;

**Domestic Priority data as claimed by applicant**

**Foreign Applications**

JAPAN 2003-072092 03/17/2003

If Required, Foreign Filing License Granted: 05/19/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

**Title**

Aligner, exposing method, method for manufacturing thin-film transistor, display device, and electronic device using shading means



Preliminary Class

430

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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